

**PATENT APPLICATION  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application Of )

Hisato Oyamatsu )

Div. of Serial No.: 09/418,035 )

Filed: Herewith )

For: A SEMICONDUCTOR DEVICE )  
WITH AN IMPROVED GATE )  
ELECTRODE PATTERN AND A METHOD )  
OF MANUFACTURING THE SAME )

Group Art Unit: 2811

Examiner: T. Tran

Atty. Dkt. No. 01701.00188

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PRELIMINARY AMENDMENT**

The Applicant requests that the following amendments be entered prior to an action on the merits.

**Amendment to the Specification** begins on page 2 of this paper.

**Amendments to the Claims** begins on page 3 of this paper.

**Remarks** begin on page 4 of this paper.